

09/830963 #6

JC 8 Rec'd PCT/PTO

03 MAY 2001 A

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION OF

Inventor(s): KAWASAKI, Yogo et al

Filed: Herewith

Title: MULTI-LAYER RINTED CIRCUIT BOARD AND METHOD OF MANUFACTURING
MULTI-LAYER PRINTED CIRCUIT BOARD

May 3, 2001

PRELIMINARY AMENDMENTHon. Commissioner of Patents
Washington, D.C. 20231

Sir:

Please amend this application as follows:

IN THE SPECIFICATION:

At the top of the first page, just under the title, insert

☒ --This application is the National Phase of International Application
PCT/IP00/07037 filed October 10, 2000 which designated the U.S.
and that International Application

☐ was ☒ was not published under PCT Article 21(2) in English.--

Respectfully submitted,

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